

PG177-A00

TU117, 128b GDDR6 2CH x16, 75W/90W
DVI-D DL + HDMI + DP

TABLE OF CONTENTS

Page	Description	Page	Description
1	Table of Contents	26	PS: 5V
2	Block Diagram	27	PS: 1V8
3	PCI Express	28	PS: PEXVDD
4	MEMORY: GPU Partition A/B	29	PS: FBVDDQ
5	MEMORY: FBA[31:0]	30	PS: NVVDD OVR3i
6	MEMORY: FBA[63:32]	31	PS: NVVDD Phase 1,3
7	MEMORY: FBB[31:0]	32	PS: NVVDD Phase 2
8	MEMORY: FBB[63:32]	33	MECH: Bracket/Thermal
9	GPU FBVDDQ & 1V8 Decoupling	34	IFPD DP
10	GPU NVVDD DECOUPLING	35	Power Inputs and Filtering
11	IFPAB DVI-DL	36	PS: STEERING, UNDER POWER BOOT & HOT-UNPLUG
12	IFPA DP		
13	IFPB DP		
14	IFPC		
15	IFPC HDMI		
16	IFPDEF UNUSED		
17	MISC1: Fan, Thermal, JTAG, GPIO		
18	MISC2: ROM, XTAL, Straps		
19	Power Inputs and Filtering		
20	SEQ: 5V, 1V8, NV3V3 ENABLE		
21	SEQ: NV, PEX, FB ENABLE		
22	SEQ: VOLTAGE MONITOR		
23	SEQ: DISCHARGE		
24	SEQ: MISC		
25	PS: Pre-filter		


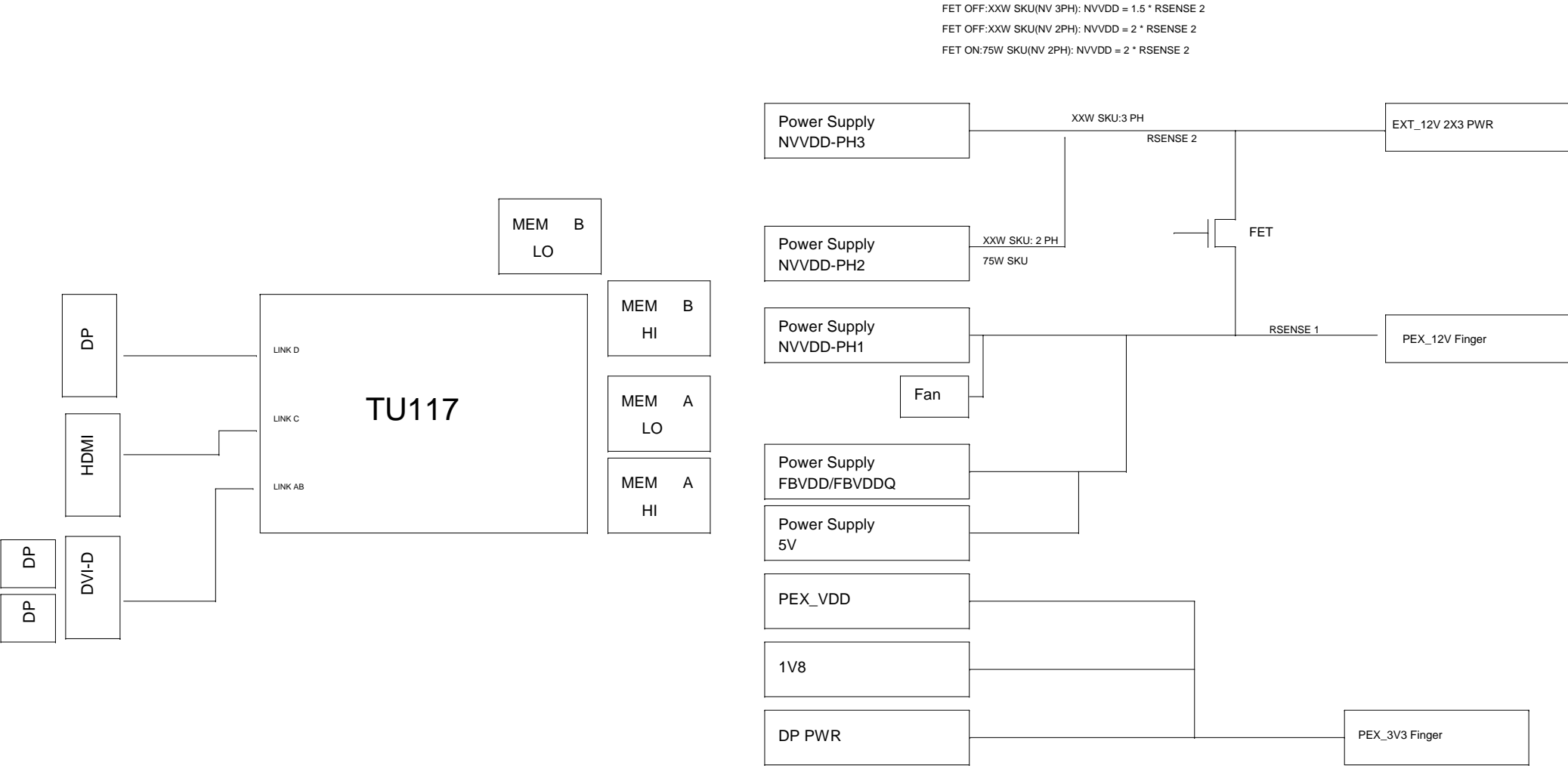


Table of Contents		
Docu	ment Number	Rev
GV-N1656WF20C-4GD		1.0
Date	Wednesday, March 11, 2020	Sheet 1 of 37

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Block Diagram



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ASSEMBLY PAGE DETAIL

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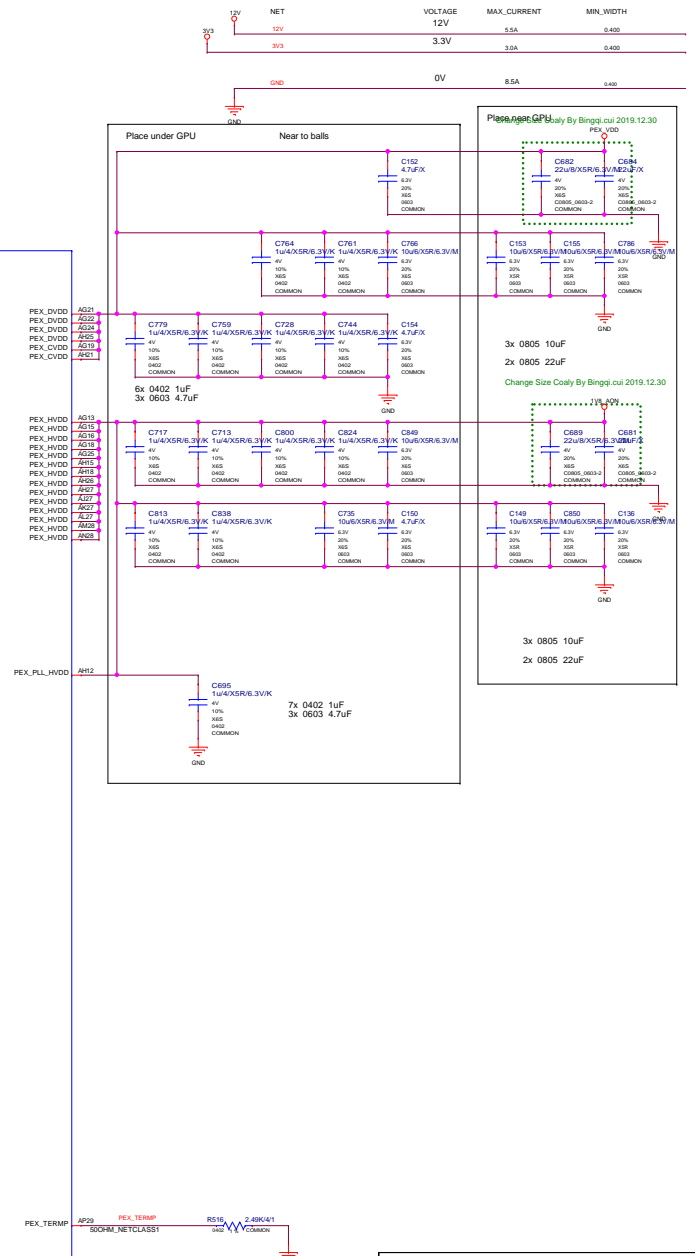
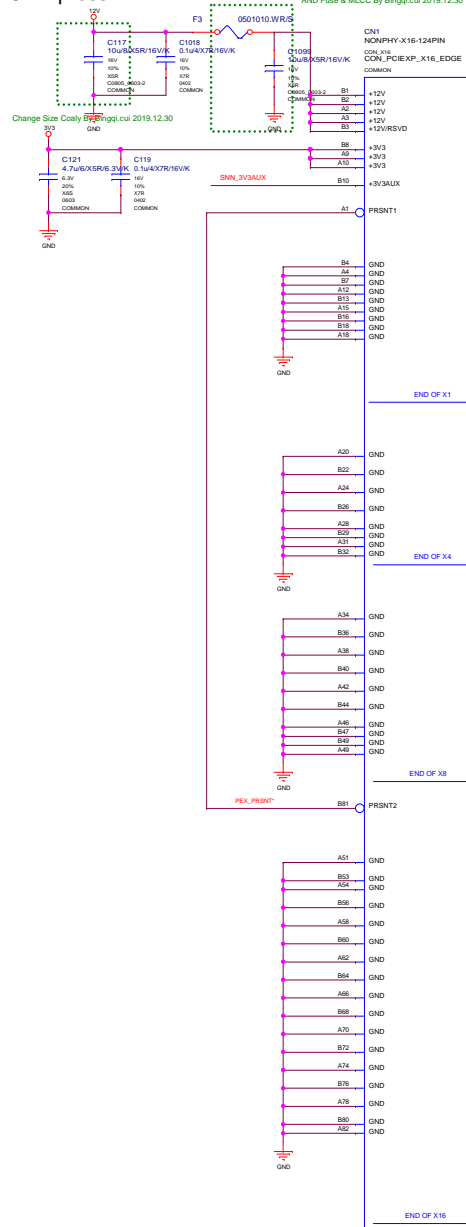
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Block Diagram

Document Number **GV-N165WF20C-4GD** Rev **1.0**

Date: **Wednesday, March 11, 2020** Sheet **2** of **37**

PCI Express

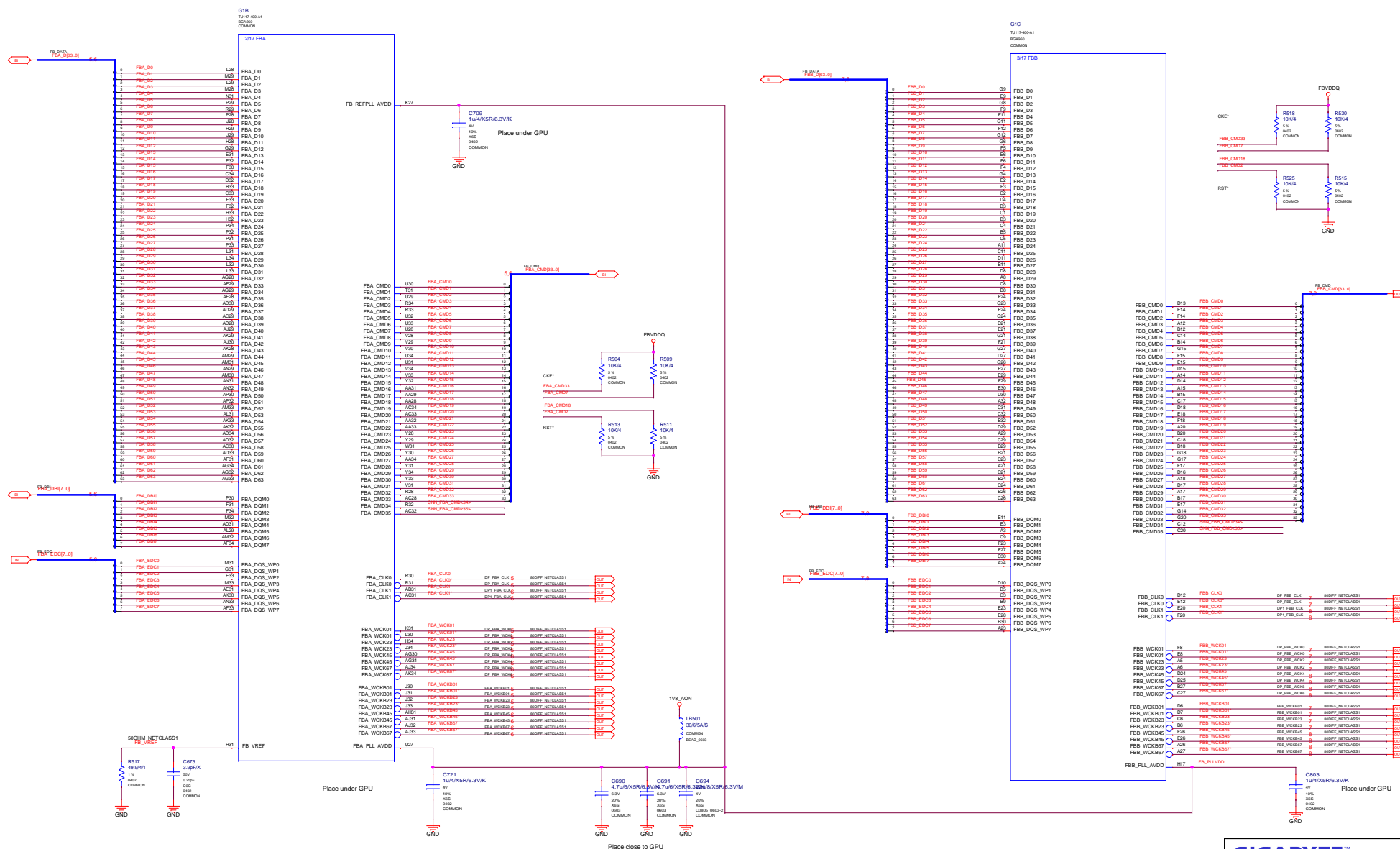


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GIGABYTE™			
Title PCI Express			
Size	Document Number		Rev
Custom	GV-N1656WF20C-4GD		1.0
Date:	Wednesday, March 11, 2020	Sheet 3 of 37	

MEMORY: GPU Partition A/B

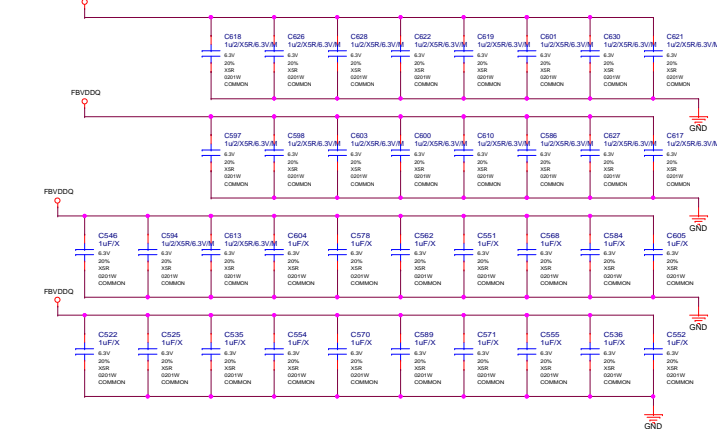
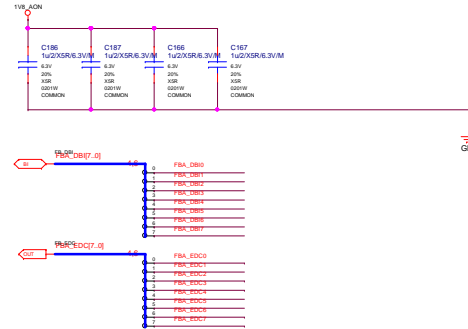
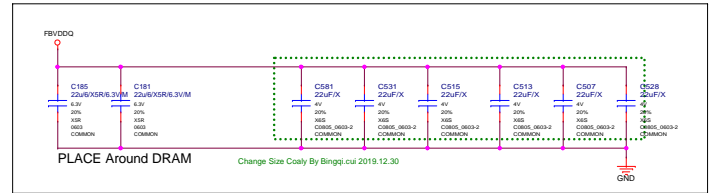
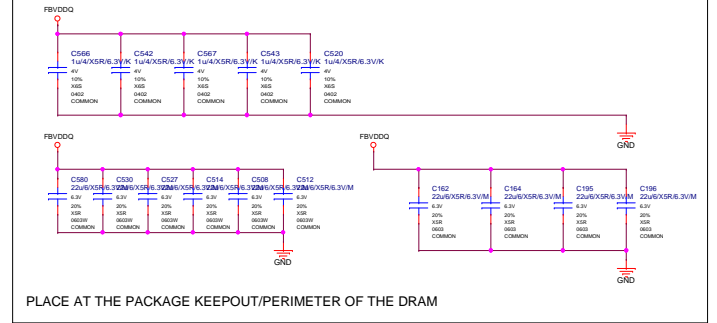
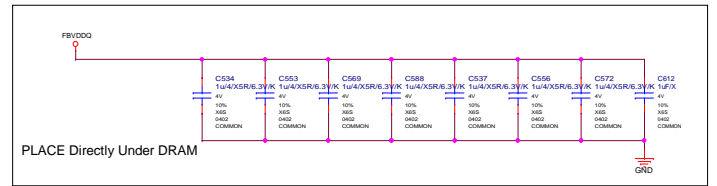
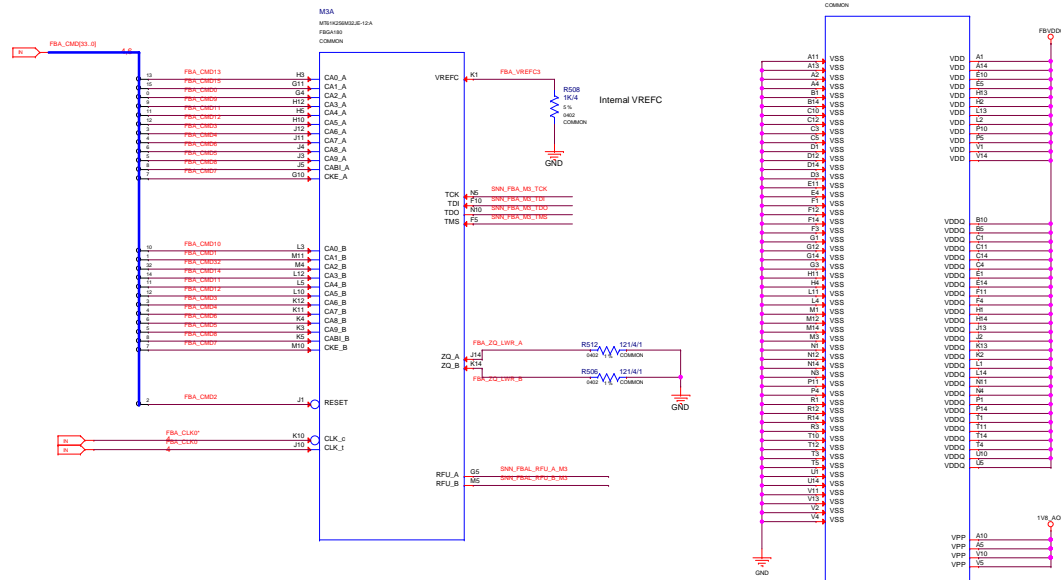


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GIGABYTE™			
Title GPU Partition A/B			
Size Custom	Document Number GV-N1656WF2OC-4GD		Rev 1.0
Date:	Wednesday, March 11, 2020	Sheet 4 of 37	

MEMORY: FBA Partition 31..0



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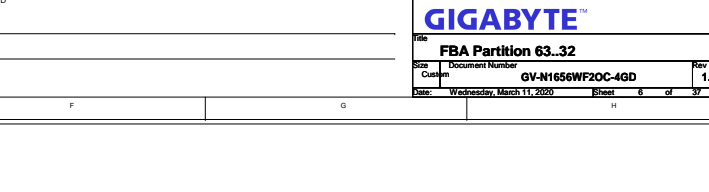
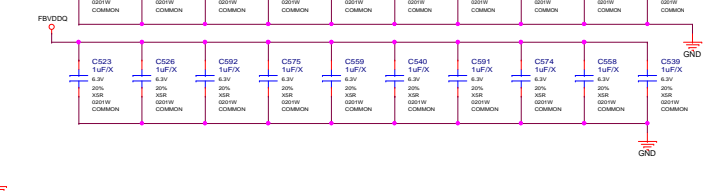
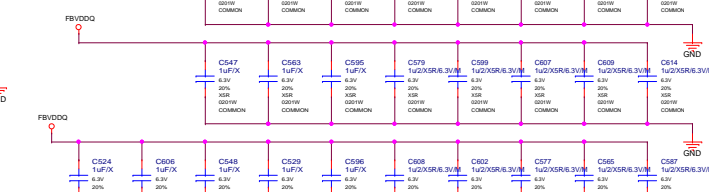
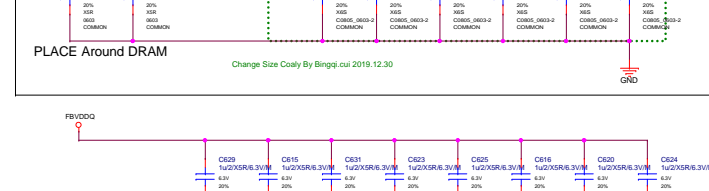
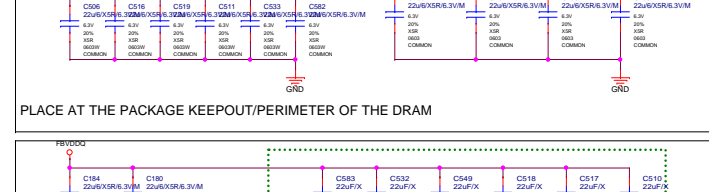
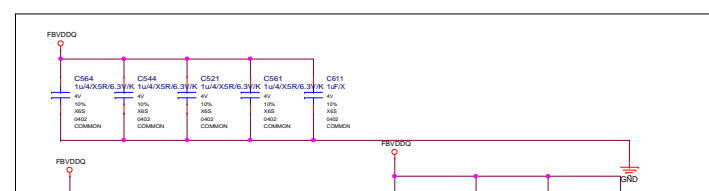
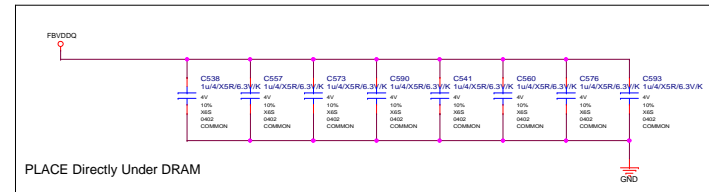
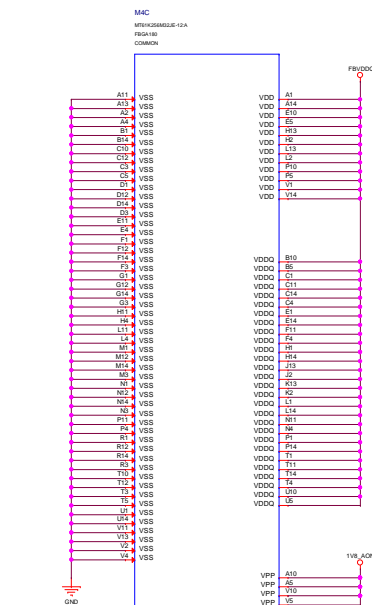
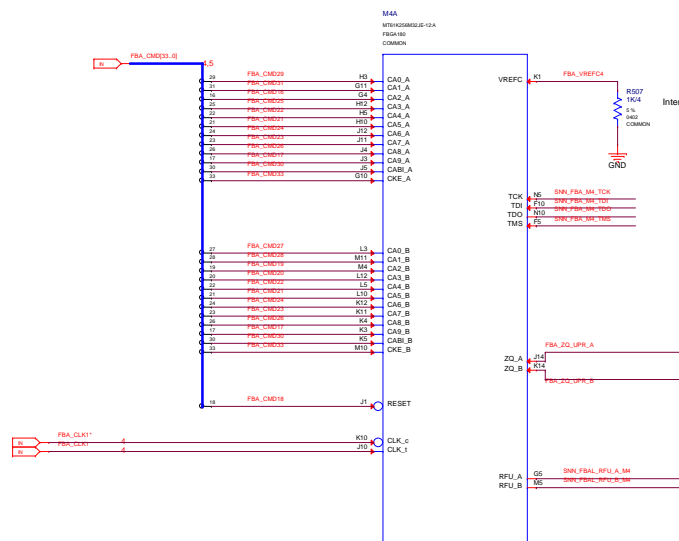
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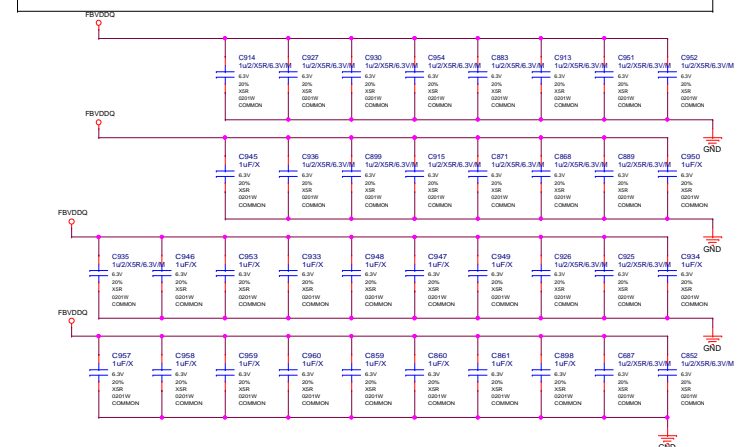
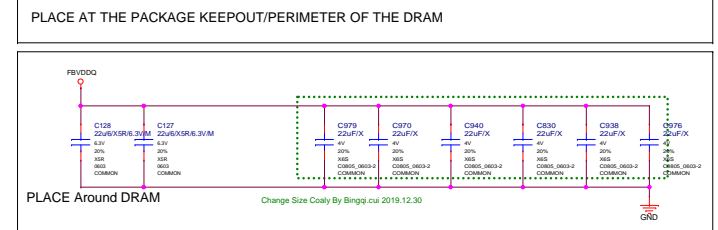
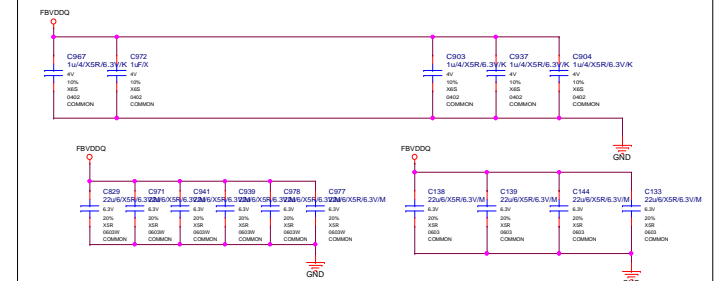
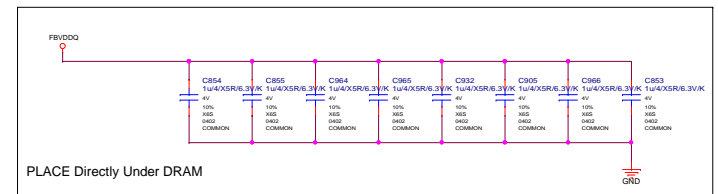
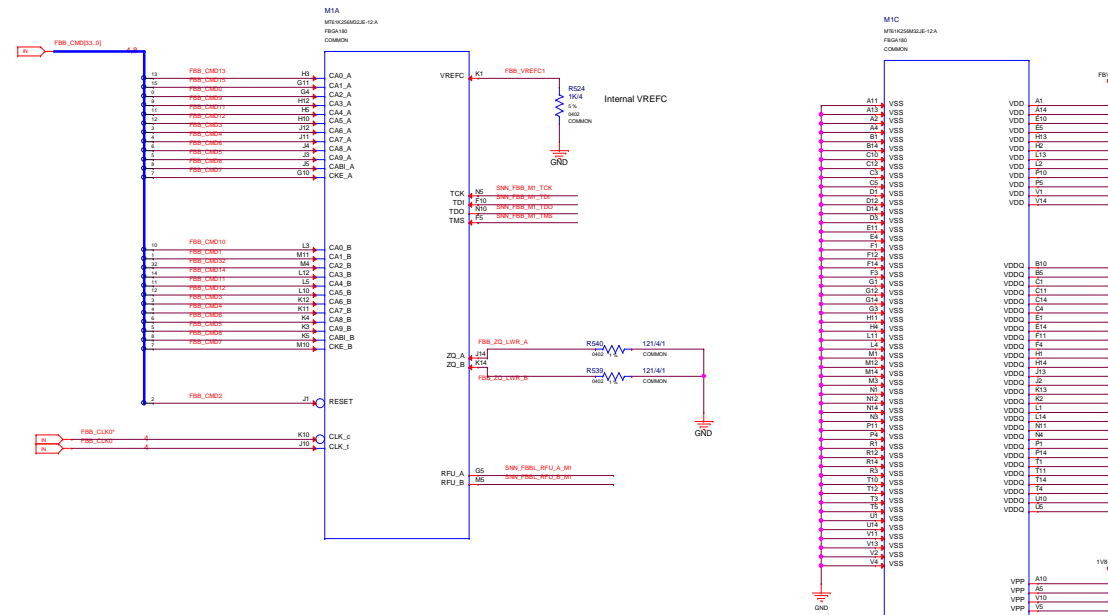
Rev: **1.0**

Date: **Wednesday, March 11, 2020** Sheet: **5 of 9**

MEMORY: FBA Partition 63..32



MEMORY: FBB Partition 31..0

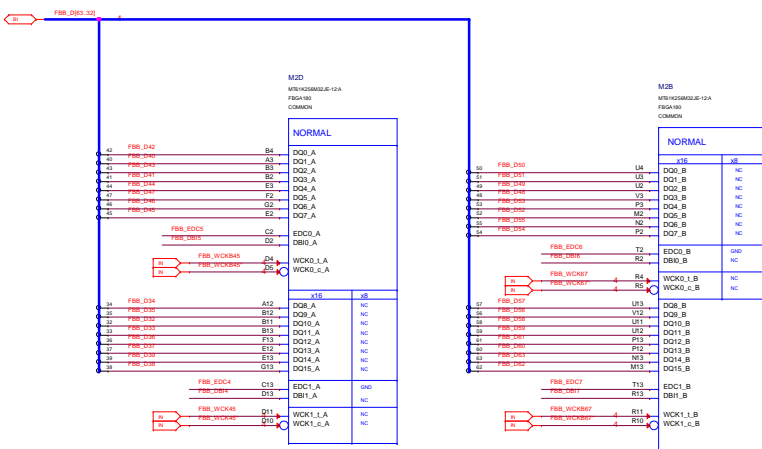
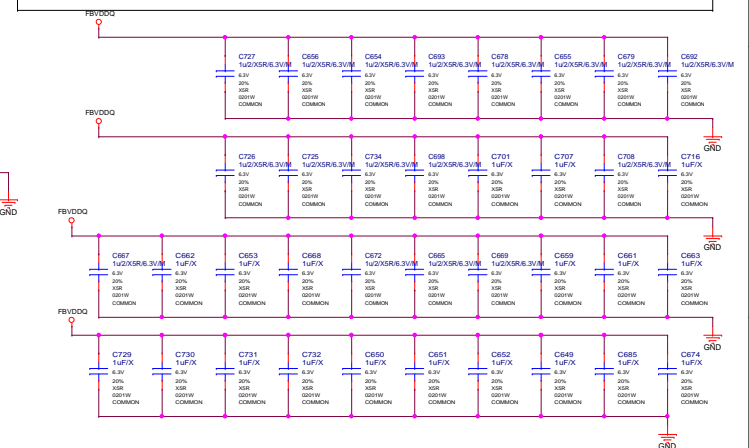
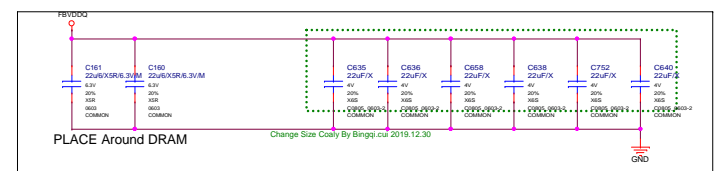
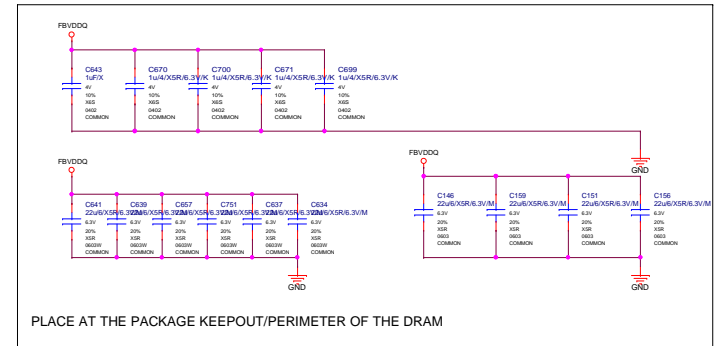
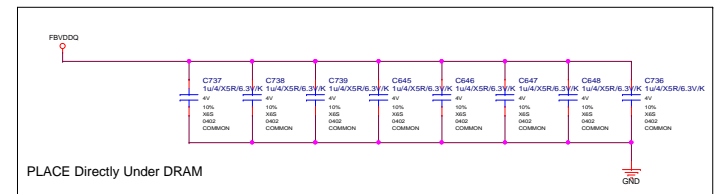
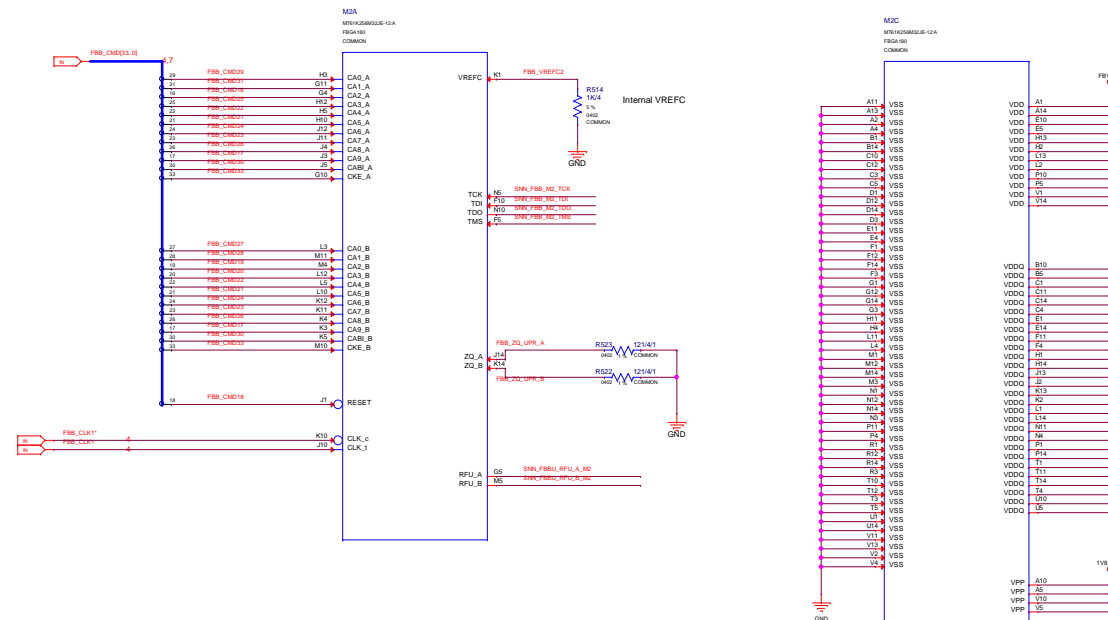


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GIGABYTE™			
Title FBB Partition 31..0			
Size	Document Number		Rev
Custom	GV-N1656WF20C-4GD		1.0
Date:	Wednesday, March 11, 2020	Sheet	7 of 37

MEMORY: FBB Partition 63..32



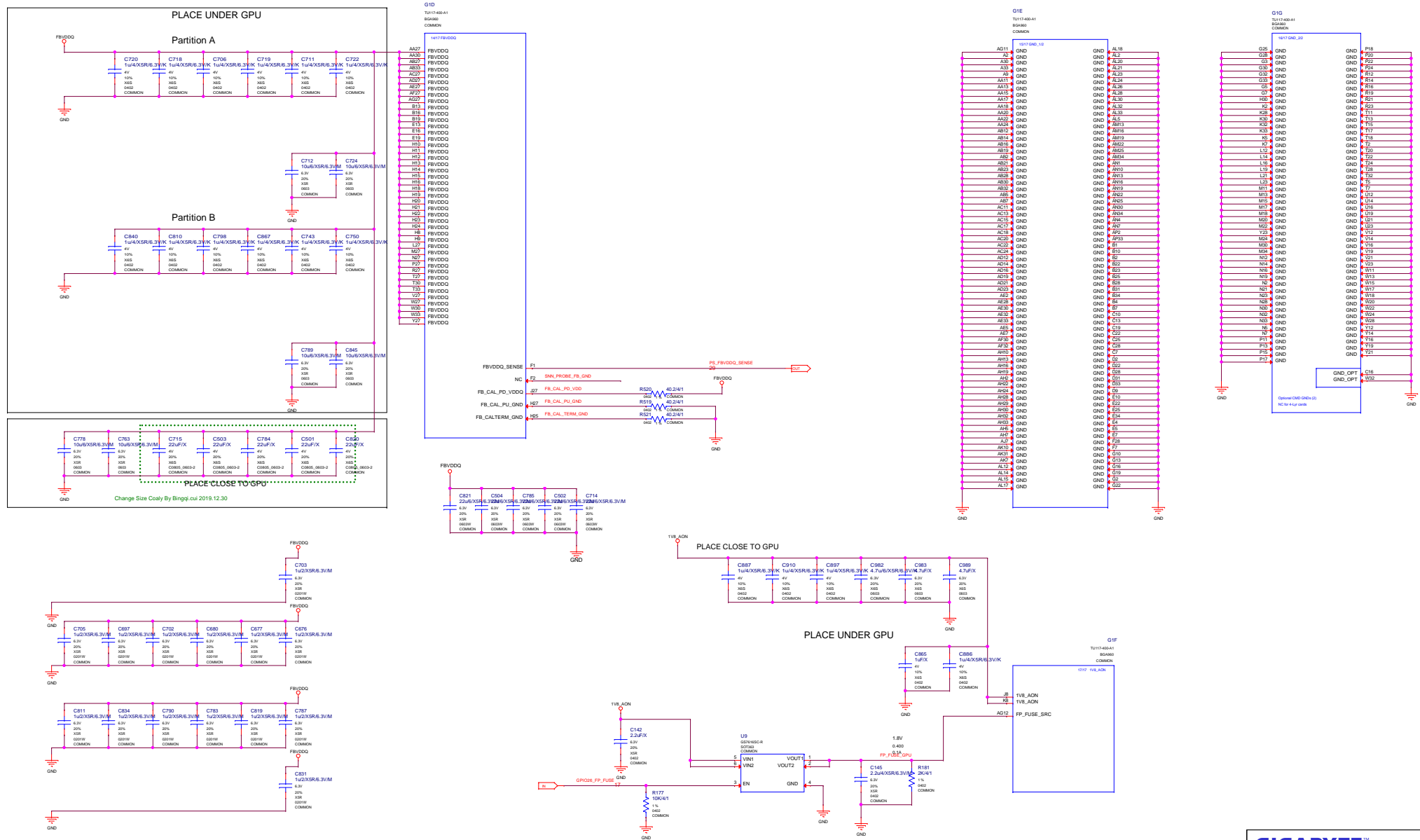
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Size	Document Number	Rev
Custom	GV-N1656WF2OC-4GD	1.0
Date:	Wednesday, March 11, 2020	Sheet 8 of 37

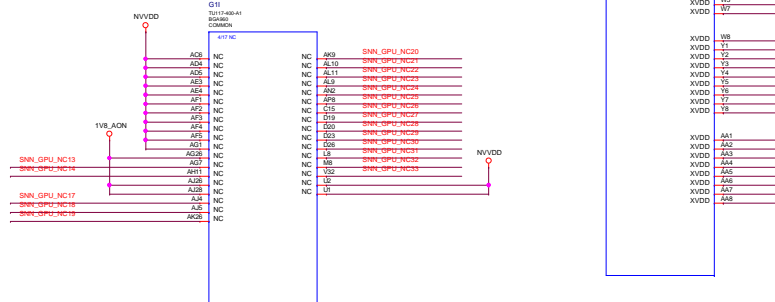
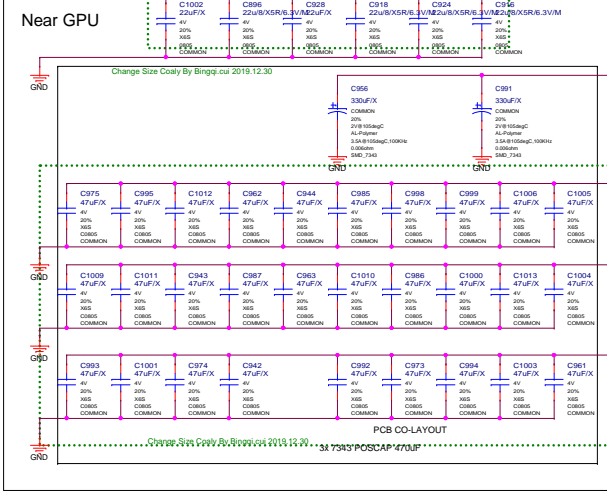
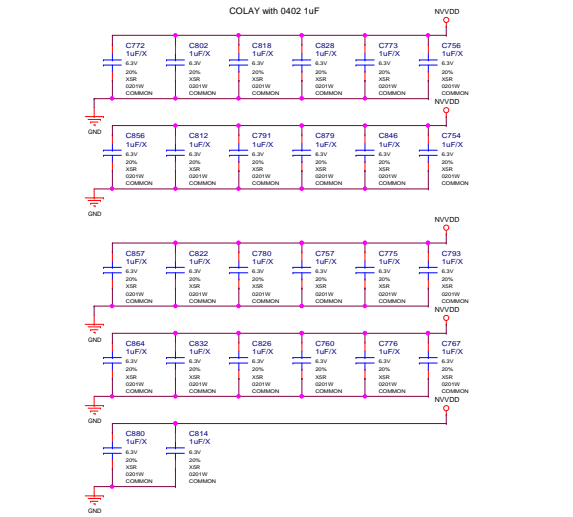
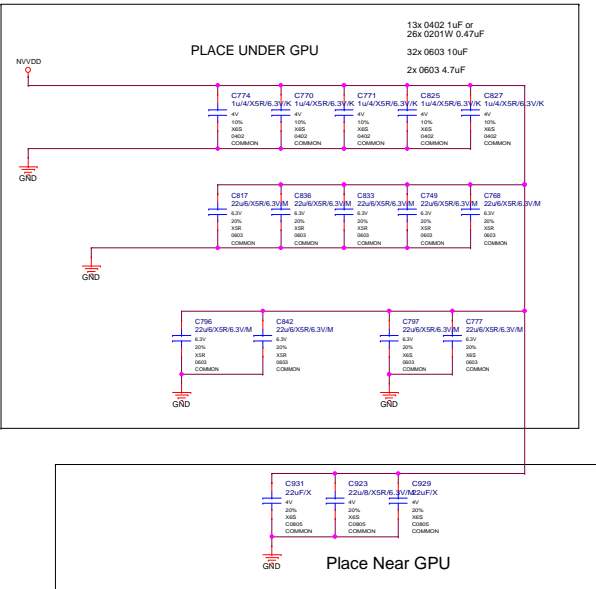
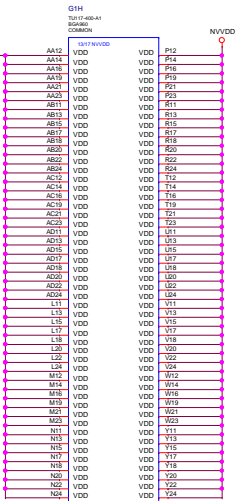
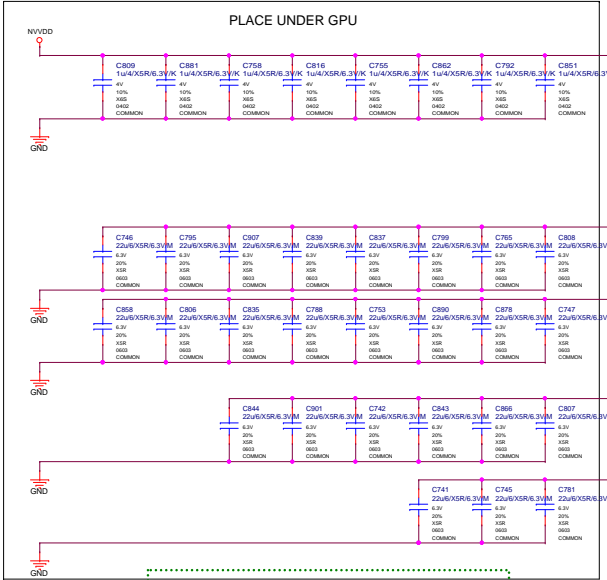
GPU FBVDDQ & 1V8 Decoupling



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PAGE DETAIL	GPU FBVDDQ & 1V8 Decoupling

GPU NVVDD Decoupling



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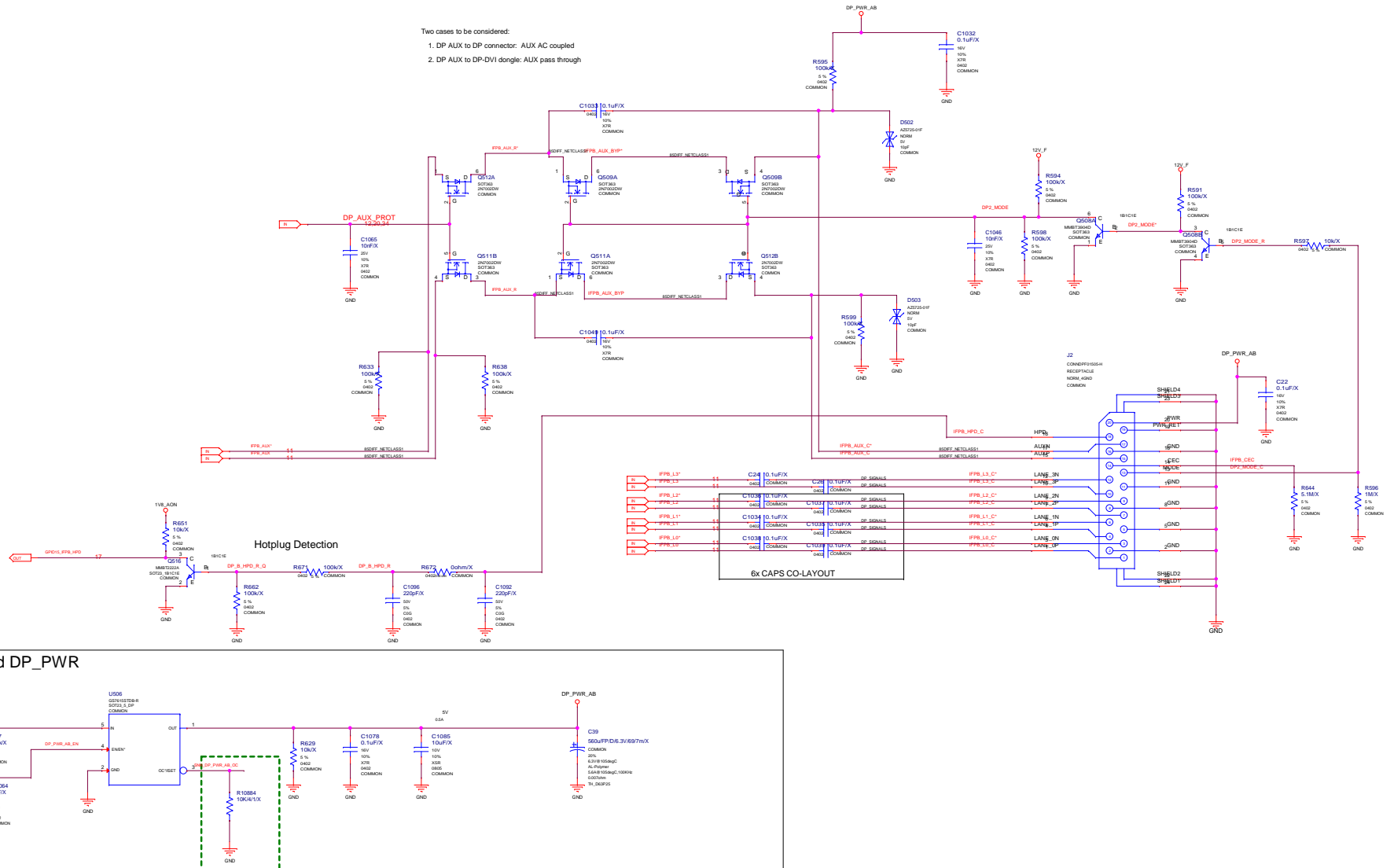
Rev: 1.0

Customer: GV-N1656WF2OC-4GD

Date: Wednesday, March 11, 2020 Sheet: 10 of 37

IFPB DP CO-LAYOUT WITH IFPAB DVI-D-DL

Page13: IFPB DP



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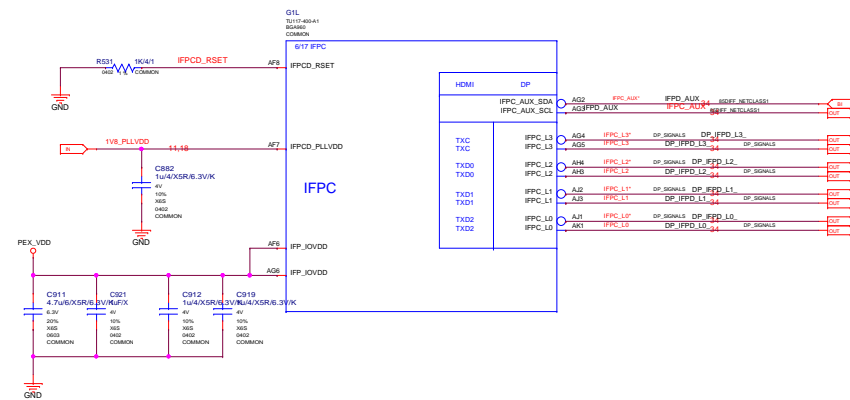
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Size	Document Number
Custom	GV-N1656WF20C-4GD

Date: Wednesday, March 11, 2020 Sheet 13 of 37

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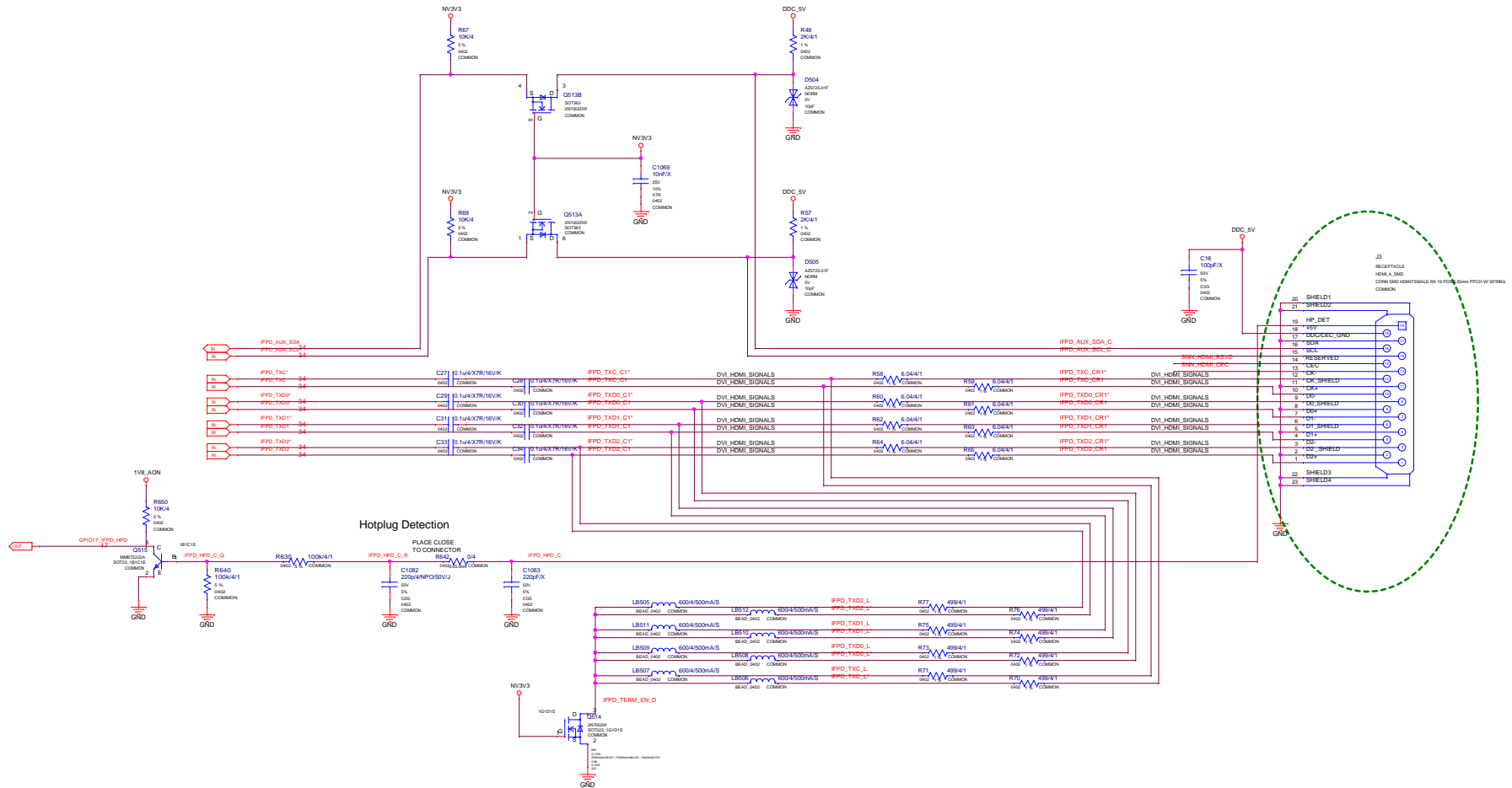


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GIGABYTE™			
Title			
IFPC			
Size	Document Number	Rev	
Custom	GV-N1656WF20C-4GD	1.0	
Date:	Wednesday, March 11, 2020	Sheet	14 of 37

IFPC HDMI

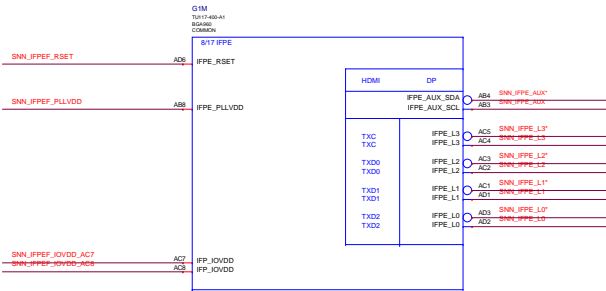


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GIGABYTE™

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IFPC HDMI			
Size	Document Number	Rev	
Custom	GV-N1656WF20C-4GD	1.0	
Date:	Wednesday, March 11, 2020	Sheet	15 of 37

IFPDEF UNUSED



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ASSEMBLY
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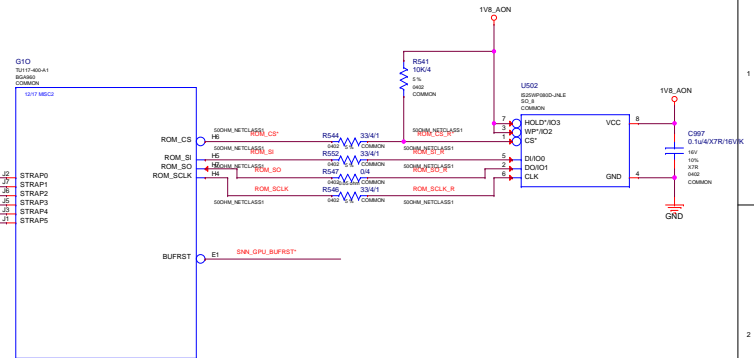
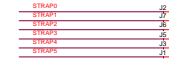
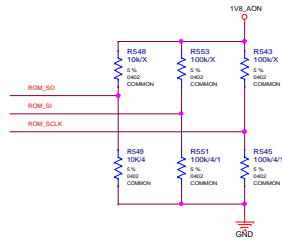
Document Number
GV-N1656WF20C-4GD

Date: Wednesday, March 11, 2020 Sheet 16 of 37

Rev
1.0

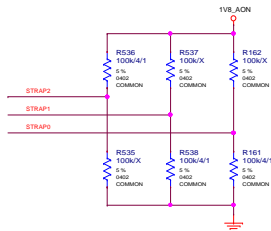
MISC2: ROM, XTAL, Straps

ROM_SO	ROM_SI	ROM_SCLK	DUMMY[2:0]_FS_OVERT	1:ENABLE 0:DISABLE	
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L	L	M	xxx0	FS_OVERT DISABLE	



STRAP2	STRAP1	STRAP0	RAMCFG[4:0]	
L	L	L	0000	Samsung 8Gb 7GHz K4280325BC-HC14
L	L	H	0001	Micron 8Gb 7GHz MT61K256M32JE-14-A
L	H	L	0010	Hynix 8Gb 7GHz H56C8H24MJR-S2C
L	H	H	0011	Samsung 8Gb 6GHz K4280325BC-HC12
H	L	L	0100	Micron 8Gb 6GHz MT61K256M32JE-12-A
H	L	H	0101	Hynix 8Gb 6GHz H56C8H24MJR-S0C
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H	H	H	0111	

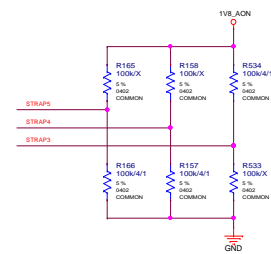
H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V



Smart Fan

XTALOUTBUFF	Voltage	SmartFan PWM	GPIO/PWM at the Fan
Low	0V	GPIO Disabled	100%
Middle	0.9V	66% PWM	33% PWM (Typical)
High	1.8V	33% PWM	66% PWM

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
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L	H	L	0	0	1	0
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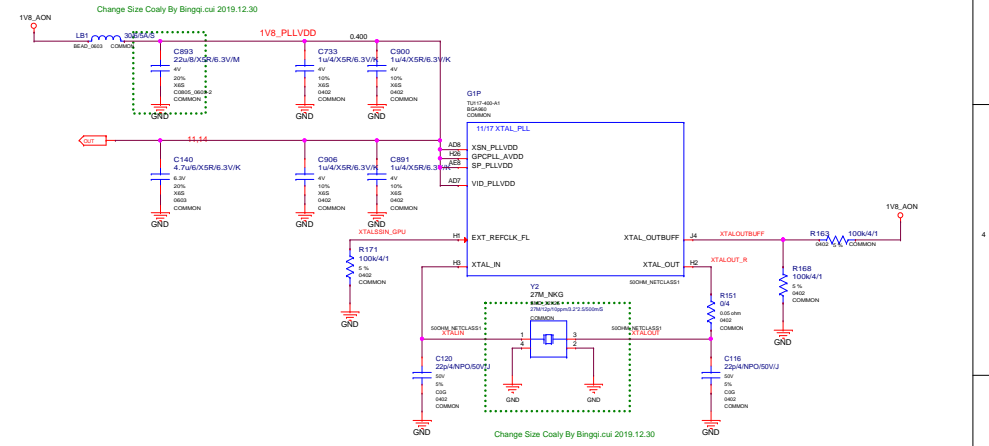


```
1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGNAL

1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE
```



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MISC2: ROM, XTAL, Straps

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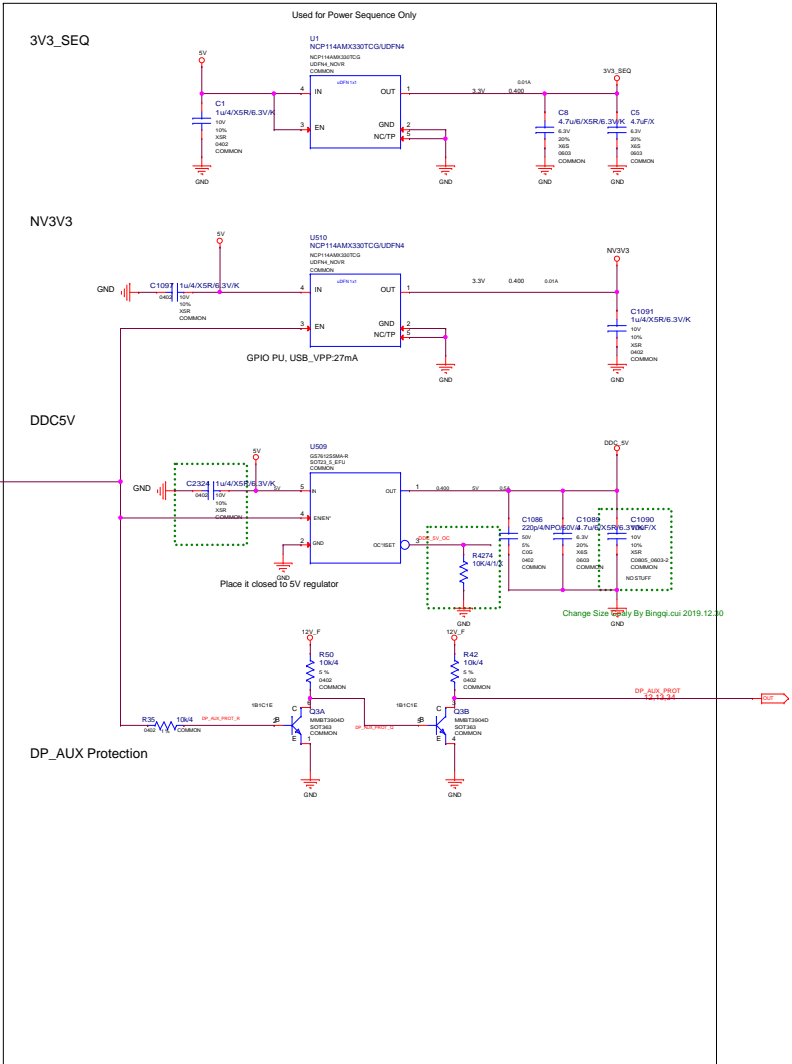
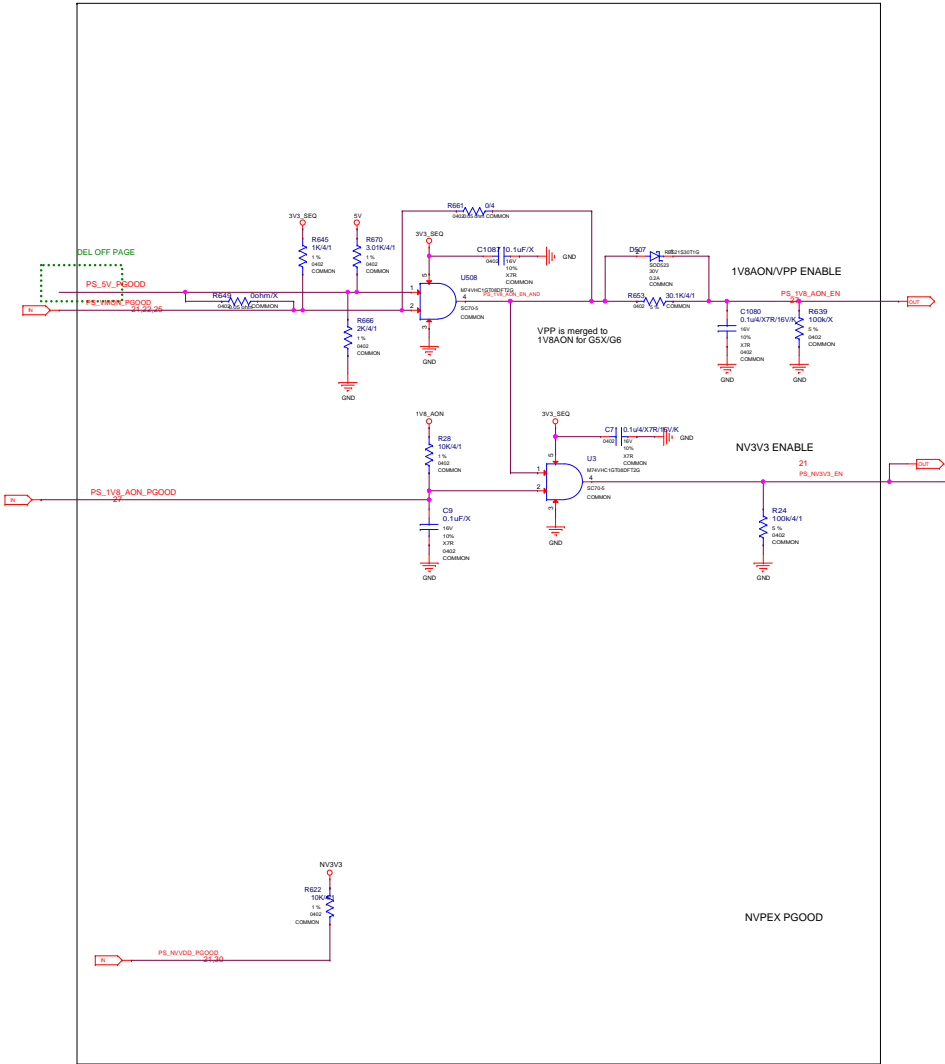
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Title ROM, XTAL, Straps			
Size	Document Number	Rev	
Custom	GV-N1656WF2OC-4GD	1.0	
Date:	Wednesday, March 11, 2020	Sheet	18 of 37

GIGABYTE™			
Title			
Power Inputs and Filtering			
Size	Document Number		Rev
Custom	GV-N1656WF2OC-4GD		1.0
Date:	Wednesday, March 11, 2020	Sheet	19 of 37

Size	Document Number	Rev
Custom	GV-N1656WF20C-4GD	1.0
Date:	Wednesday, March 11, 2020	Sheet 19 of 37

Size	Document Number	Rev
Custom	GV-N1656WF20C-4GD	1.0
Date:	Wednesday, March 11, 2020	Sheet 19 of 37

SEQ:5V,1V8,NV3V3 ENABLE

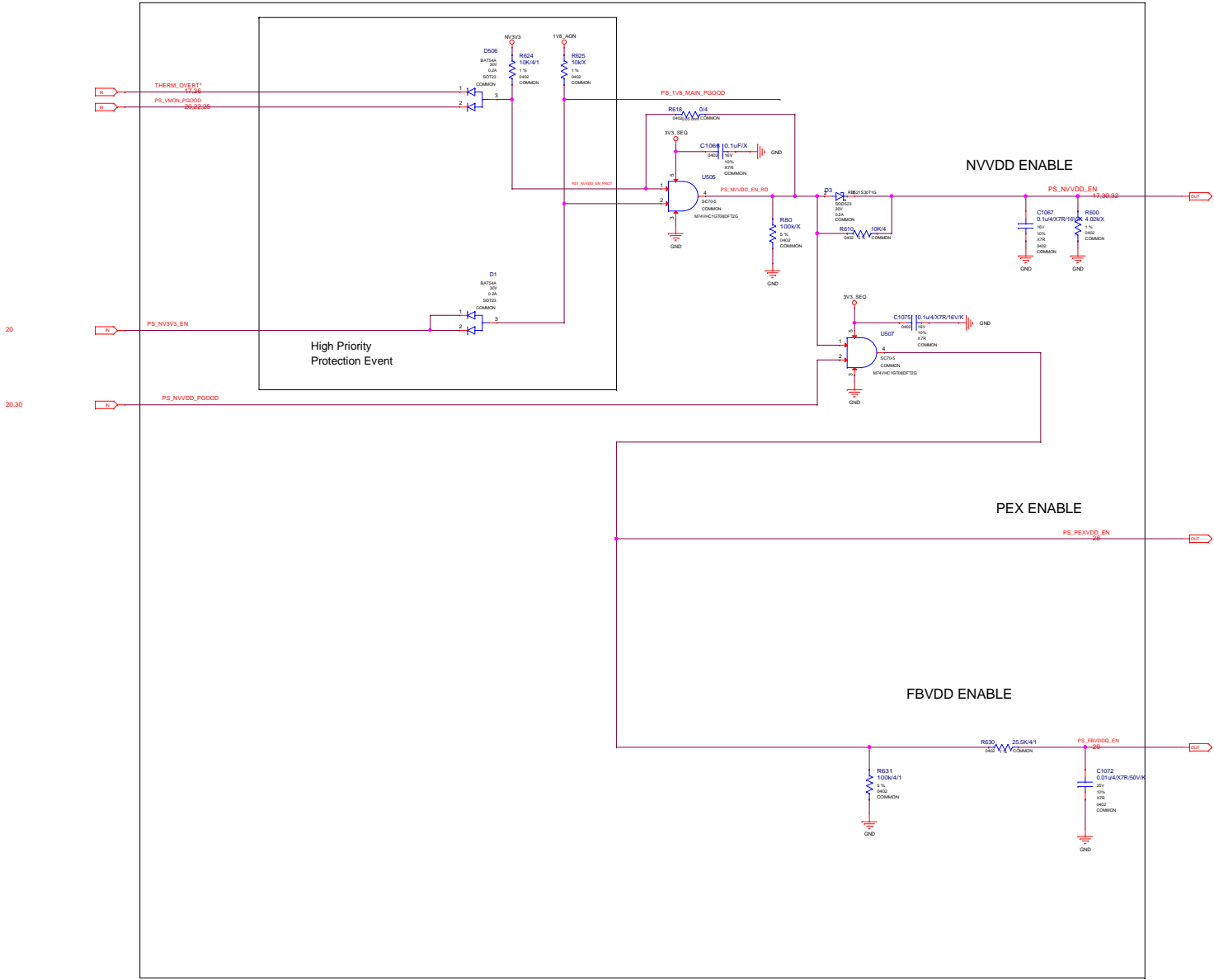


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ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	SEQ: 5V, 1V8, NV3V3 ENABLE

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File	
5V,1V8,NV3V3 ENABLE	
Size	Document Number
GV-N1656WF20C-4GD	Rev 1.0
Date	Wednesday, March 11, 2020
Sheet	20 of 37

SEQ:NV,PEX,FB ENABLE



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ASSEMBLY
PAGE DETAIL

ASSEMBLY DESCRIPTION
SEQ: NV, PEX, FB ENABLE

GIGABYTE™

File
NV,PEX,FB ENABLE

Size
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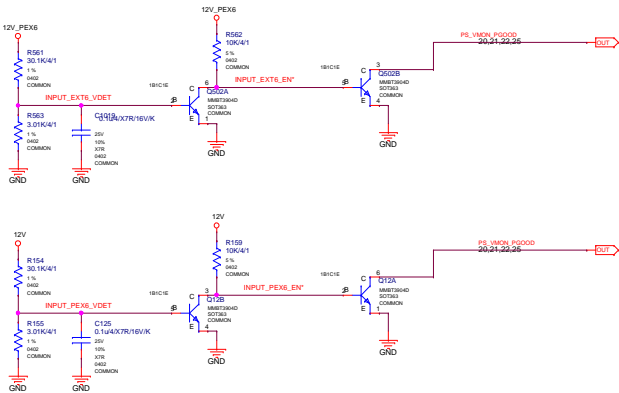
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Wednesday, March 11, 2020

Rev
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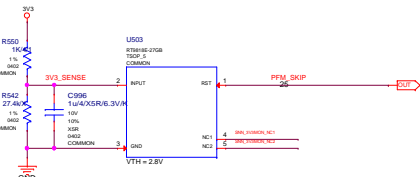
Document Number
GV-N1656WF20C-4GD

Sheet
21 of 37

SEQUENCE:Voltage Monitor



Route the trace to PEX Golden Finger



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ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	SEQ. VOLTAGE MONITOR

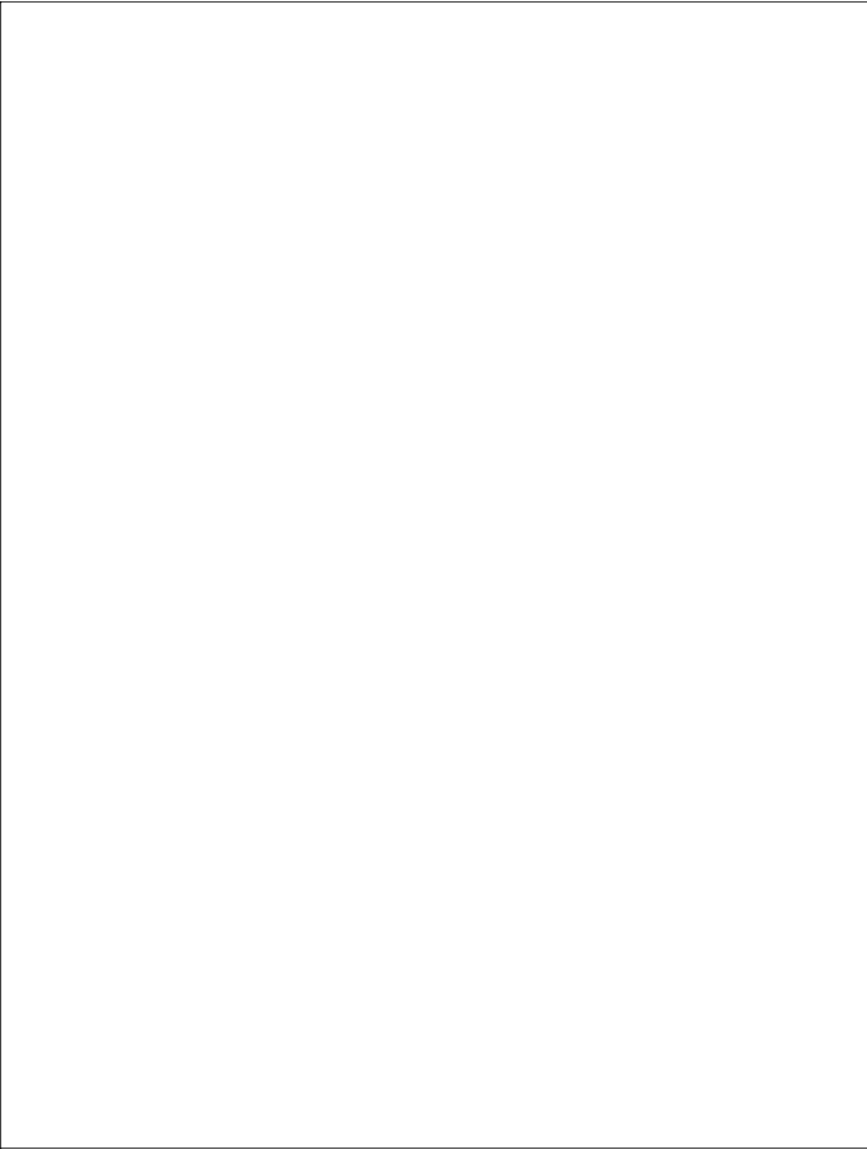
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Voltage Monitor

Document Number: GV-N1656WF20C-4GD Rev: 1.0

Date: Wednesday, March 11, 2020 Sheet: 22 of 37

SEQUENCE:Discharge



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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE DETAIL	SEQ. DISCHARGE

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Discharge

Doc Number	GV-N1656WF20C-4GD	Rev 1.0
Date	Wednesday, March 11, 2020	Sheet 23 of 37

A

B

C

D

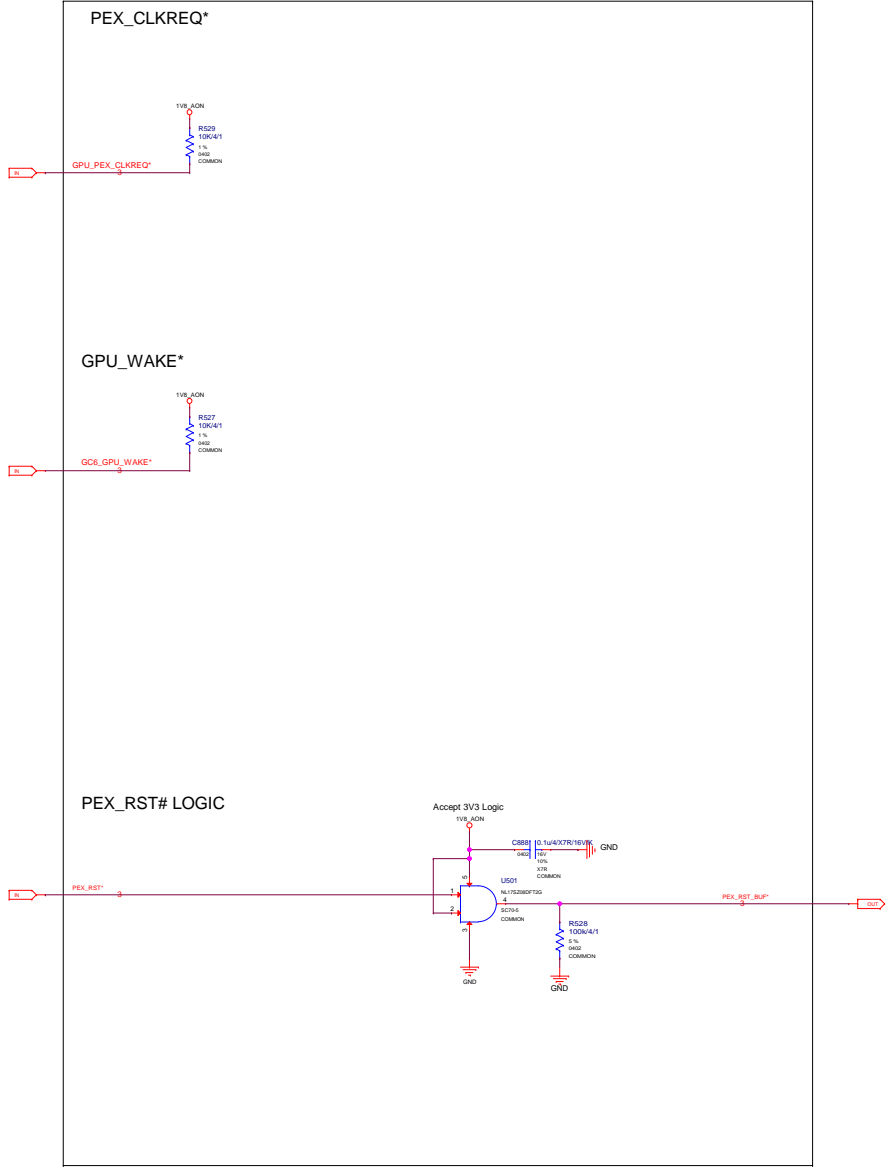
E

F

G

H

SEQUENCE:MISC



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ASSEMBLY
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SEQ: MISC

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SEQUENCE:MISC

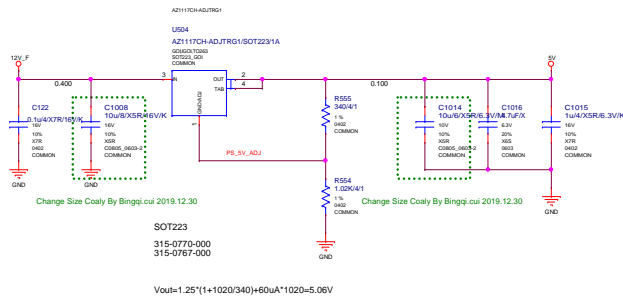
Size: Custom Document Number: GV-N1656WF20C-4GD Rev: 1.0
Date: Wednesday, March 11, 2020 Sheet: 24 of 37



ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY

Title			
PWR SENSE			
Size	Document Number	Rev	
Custom	GV-N1656WF20C-4GD	1.0	
Date:	Wednesday, March 11, 2020	Sheet	28 of 37

PS: 5V



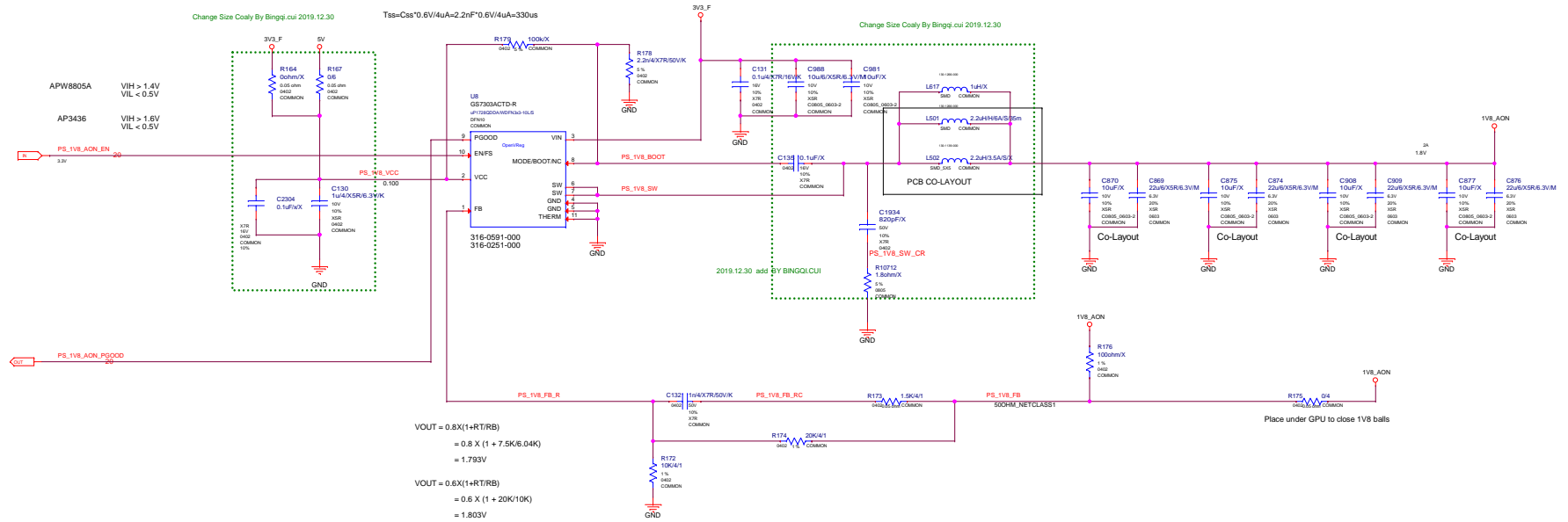
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ASSEMBLY	ASSEMBLY DESCRIPTION
PS: 5V	

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File	5V
Size	Custom
Document Number	GV-N1656WF20C-4GD
Date	Wednesday, March 11, 2020
Sheet	26 of 37
Rev	1.0

PS: 1V8

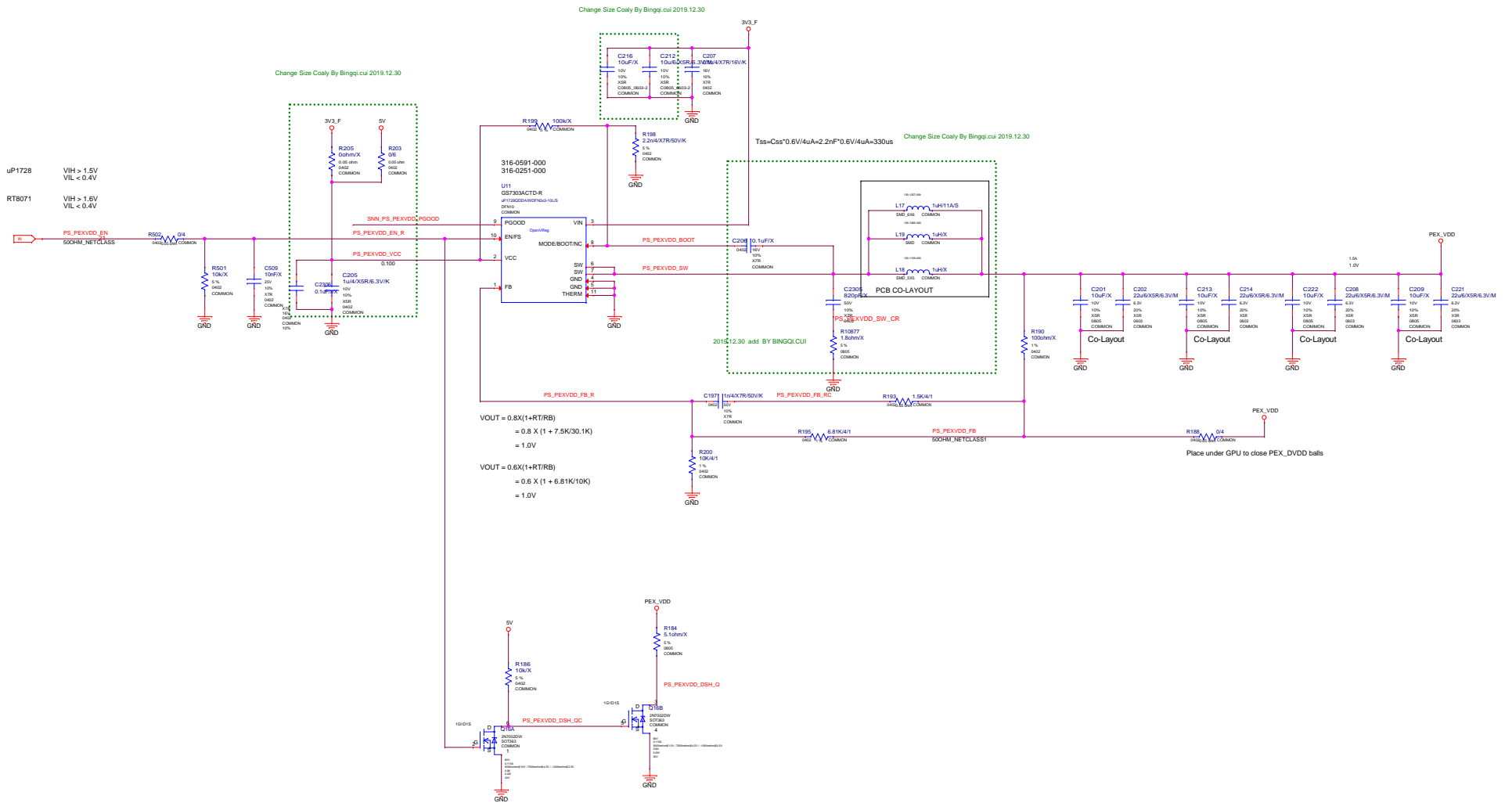


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PAGE DETAIL	PS: 1V8

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Title 1V8			
Size	Document Number		Rev
Custom	GV-N1656WF2OC-4GD		1.0
Date:	Wednesday, March 11, 2020	Sheet 27 of 37	

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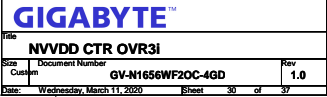


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Title PEXVDD			
Size	Document Number	Rev	
Custom	GV-N1656WF20C-4GD	1.0	
Date:	Wednesday, March 11, 2020	Sheet	28 of 37

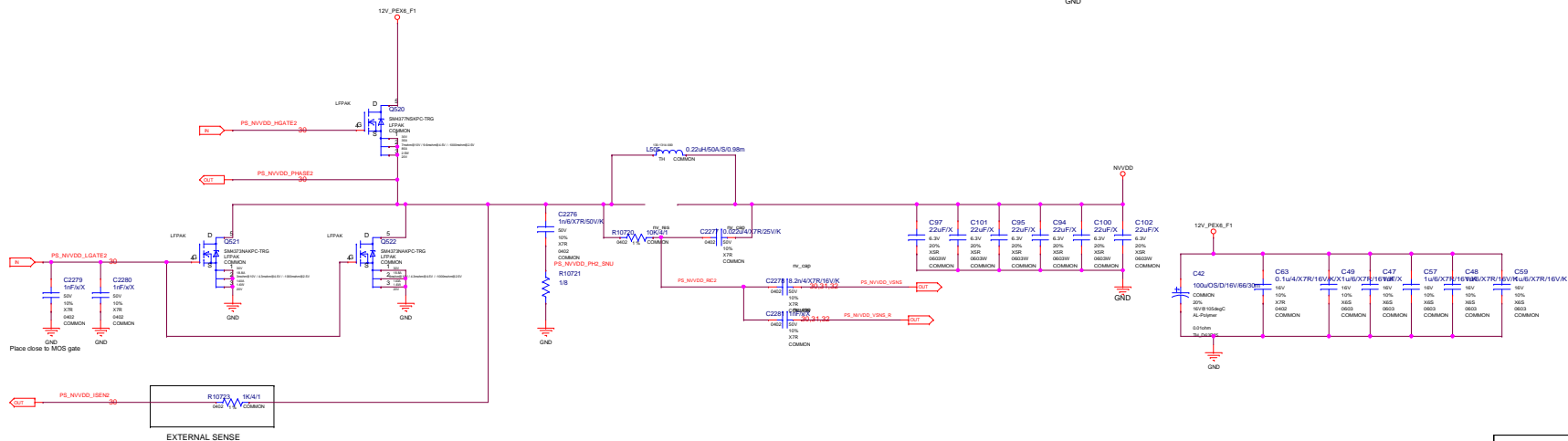
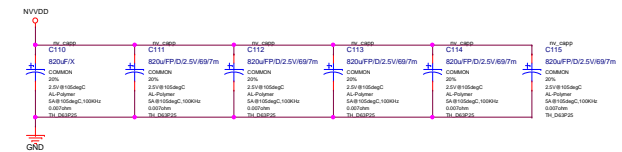
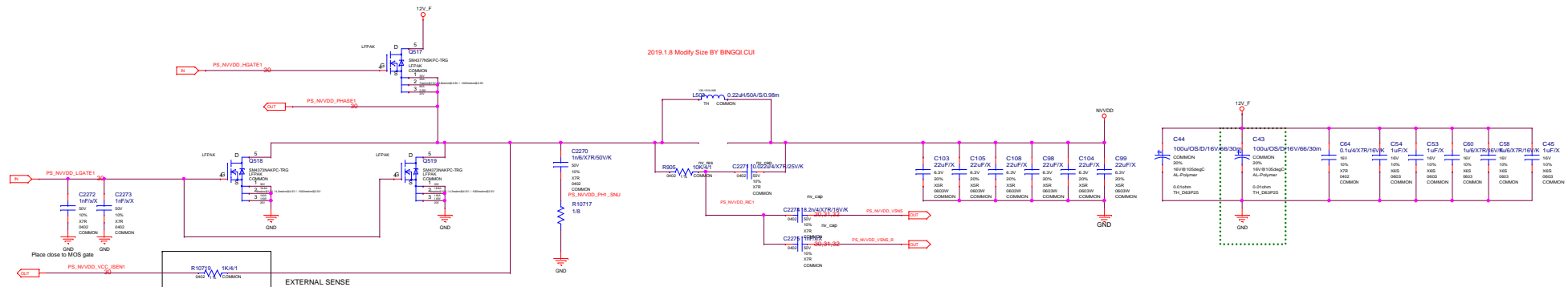
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Size	Document Number	Rev	
Custom	GV-N1656WF2OC-4GD	1.0	
Date:	Wednesday, March 11, 2020	Sheet	29 of 37



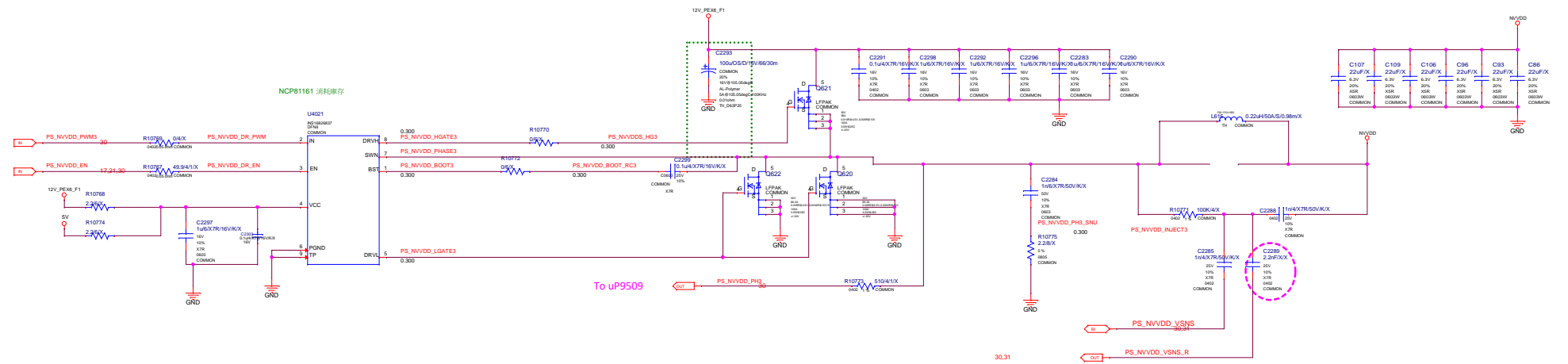
PS: NVVDD PH1&3

Change MOS By Bingqi.cui 2019.12.30



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: NVDD Phase 1,3

GIGABYTE™			
Title NVVD PH1&3			
Size	Document Number	Rev	
Custom	GV-N1656WF2OC-4GD	1.0	
Date:	Wednesday, March 11, 2020	Sheet	31 of 37

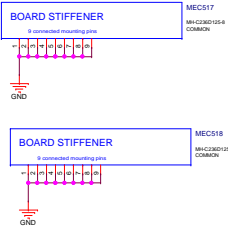


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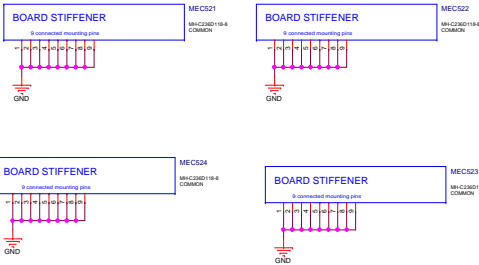
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Size	Document Number	Rev	
Custom	GV-N1656WF2OC-4GD	1.0	
Date:	Wednesday, March 11, 2020	Sheet	32 of 37

Mechanical: Bracket/Thermal Solution

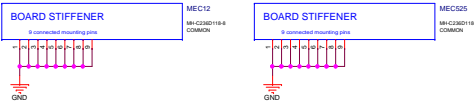
Brackets:



GPU



mos



bat



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ASSEMBLY
PAGE DETAIL

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MECH: Bracket/Thermal

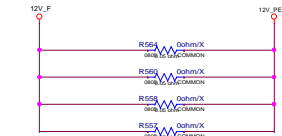
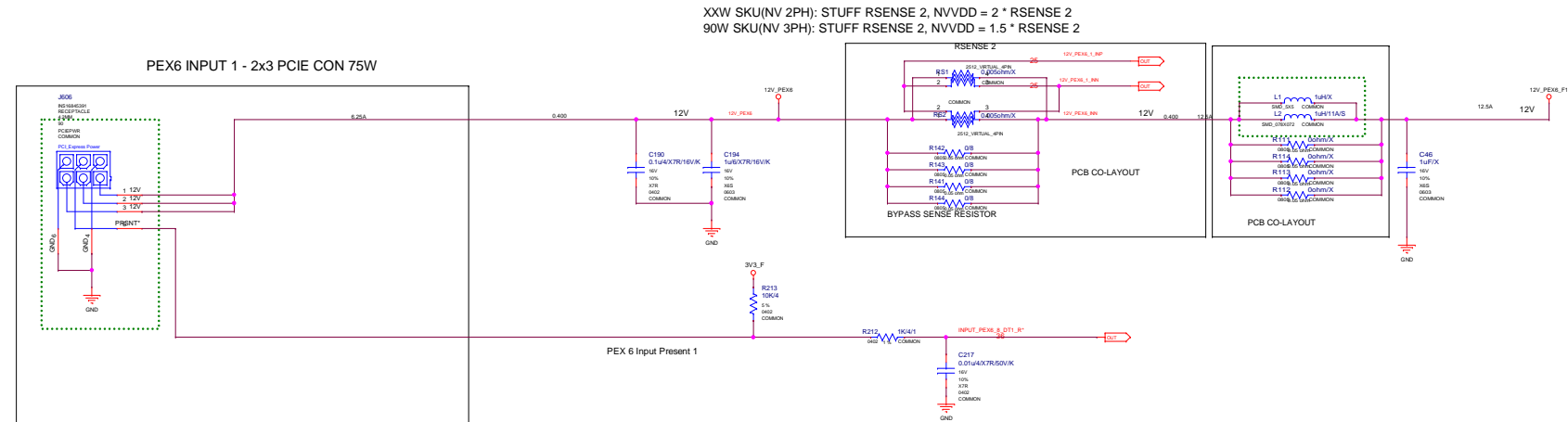
GIGABYTE™

Bracket/Thermal Solution

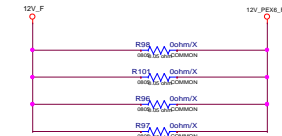
Document Number
GV-N1656WF20C-4GD Rev 1.0

Date: Wednesday, March 11, 2020 Sheet 33 of 37

Power Inputs and Filtering



Option for 75W ADC+MUX Capping SKU



75W SKU: STUFF JUMPERS

Option for 75W BA Capping SKU

Shortest physical path

ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	Power Inputs and Filtering

GIGABYTE™			
Title Power Inputs and Filtering			
Size	Document Number	Rev	
Custom	GV-N1656WF2OC-4GD	1.0	
Date:	Wednesday, March 11, 2020	Sheet	35 of 37

First: 2019-12-30 Change I/O & power

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PE STEERING, UNDER POWER BOOT & HOF-DRIP-PLUG

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Title		history	
Size	Document Number	GV-N1656WF20C-4GD	
Custom		Rev 1.0	
Date	Wednesday, March 11, 2020	Sheet 37	of 37